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ISSUE FEE IN FILE

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U.S. UTILITY Patent Application

PATENT NUMBER and

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APPL NUM 10018179	FILING DATE 05/03/2002	CLASS 257	SUBCLASS 2.60*	GAU 2811	EXAMINER H. J. H. H.
**APPLICANTS: Skotnicki Thomas; Gvrotzicki Romain;					
**CONTINUING DATA VERIFIED: #? THIS APPLICATION IS A 371 OF PCT/FR00/01537 06/05/2000 YES					
**FOREIGN APPLICATIONS VERIFIED: #? FRANCE 5907391 06/11/1999 YES					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>Hoanglam</i> #?				ATTORNEY DOCKET NO 5310-03900	
TITLE: Semiconductor device with compensated threshold voltage and method for making same 11/14/03 Formal Drawings (2 sheets) 351 5/3/02					

NOTICE OF ALLOWANCE MAILED 09/08/03		HOAI PHAM Assistant Examiner		CLAIMS ALLOWED Total Claims 20 Print Claim for 0.6 1	
ISSUE FEE 26		Date Paid 10-6003		DRAWING Sheets Draw. 2 Figs. Draw. 3 Print Fig. 1	
Amount Due 1300		Date Paid 10-6003		Application Examiner	
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